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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/784,417
	Filing Date	February 23, 2004
	First Named Inventor	Minchang Liang
	Art Unit	2822
	Examiner Name	Ida M. Soward
Total Number of Pages in This Submission	Attorney Docket Number	A1385

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input checked="" type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance communication to Group
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<input type="checkbox"/> Amendment/Reply	<input type="checkbox"/> Petition	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
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<input type="checkbox"/> Response to Missing Parts/Incomplete Application	Remarks	
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	The Commissioner is hereby authorized to charge any fee due in connection with this submission to Deposit Account No. 502942.	

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	G. Victor Treyz
Signature	<i>G. Victor Treyz</i>
Date	8/19/05

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Date	8/19/05

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A1385

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

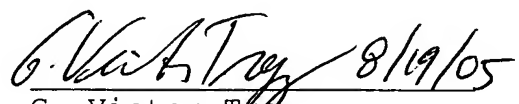
Applicants : Minchang Liang et al.
Application No. : 10/784,417
Confirmation No. : 1815
Filed : February 23, 2004
For : BIPOLAR TRANSISTORS WITH LOW BASE
RESISTANCE FOR CMOS INTEGRATED CIRCUITS
Group Art Unit : 2822
Examiner : Ida M. Soward
Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Sir:

Please substitute the enclosed formal drawings for the informal drawings originally filed with this application.

Respectfully Submitted,


G. Victor Treyz
Reg. No. 36,294
Attorney for Applicants
Customer No. 45851